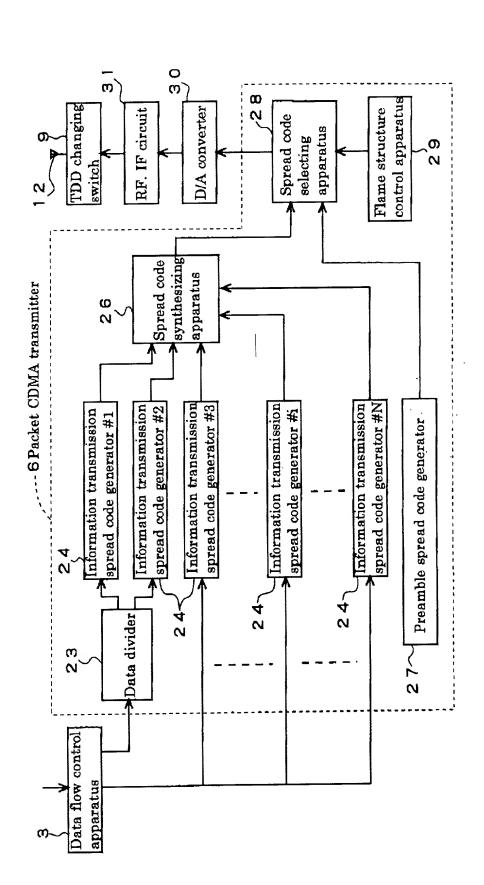
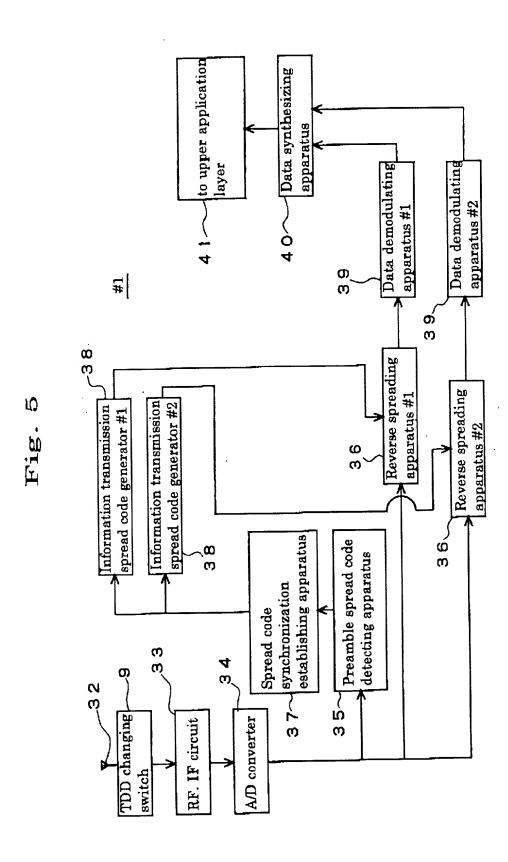
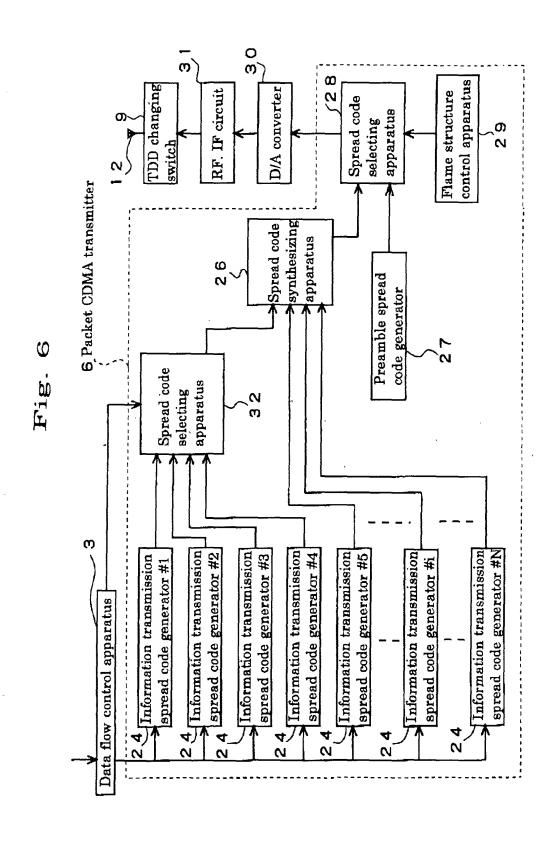


AAA 22 A AAAA 20 ⋖ n AAAAAA 1 flame 18 AAAAA 2, <u>6</u> AAAAA 2, 21 ⋖ 4

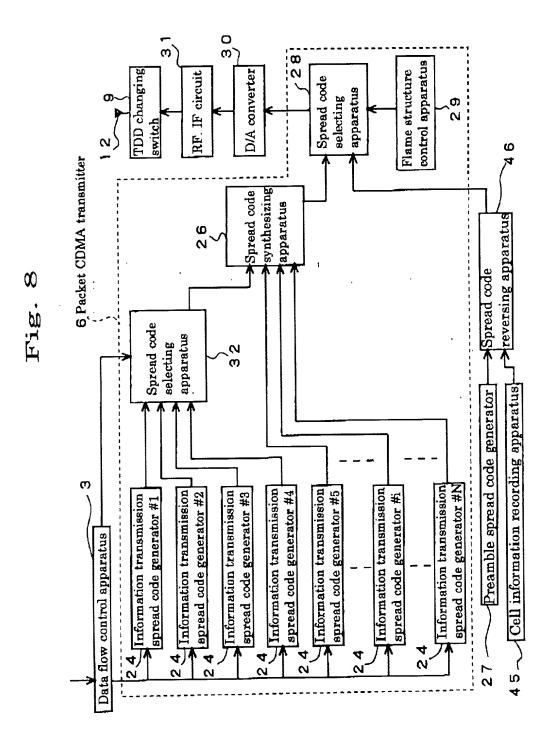
Fig. 4

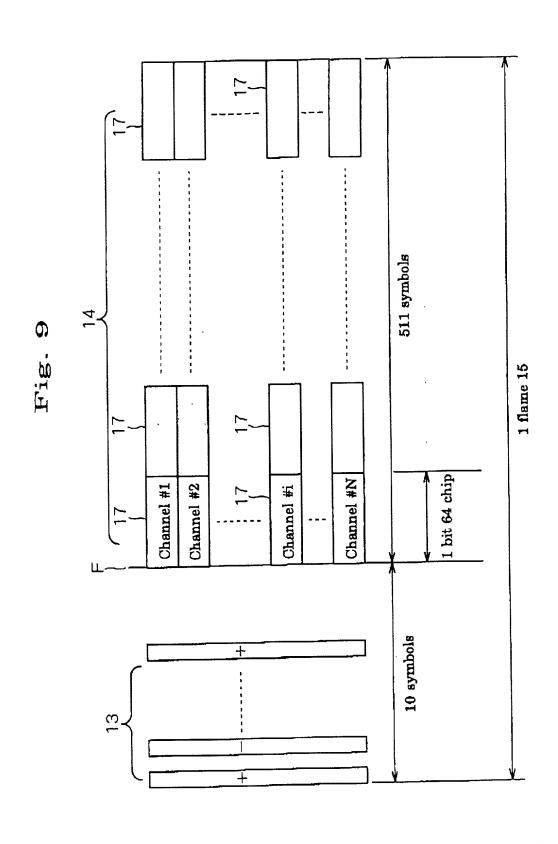






to upper application layer comparing apparatus Data demodulating Integrated value apparatus 4) u 4) N network #2 network #3 network #4 network #1 Integral Integral Integral Integral 744 **4** (2) 430 <u>မ</u> 3,6 φ (Θ ဖ ဗ Reverse spreading Reverse spreading Reverse spreading Reverse spreading apparatus #3 apparatus #4 apparatus #2 apparatus #1 1 Fig. 3 8 Information transmission 8 Information transmission spread code generator #4 Information transmission spread code generator #2 spread code generator #3 3 8 Information transmission spread code generator #1 35 **2** 6 Spread code synchronization Preamble spread code detecting apparatus establishing apparatus Φ თ ന ო ო ω) 4 TDD changing A/D converter RF. IF circuit switch ე დ





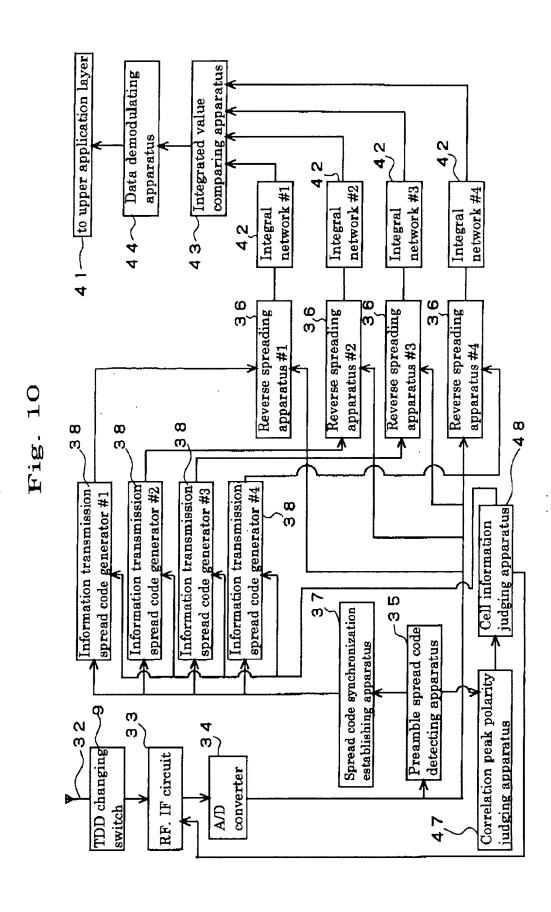
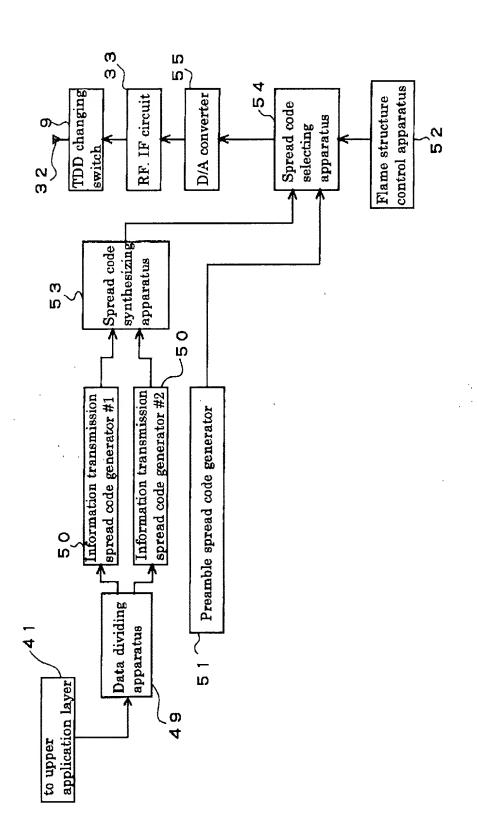
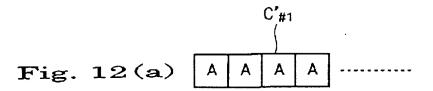


Fig. 11





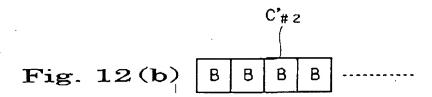
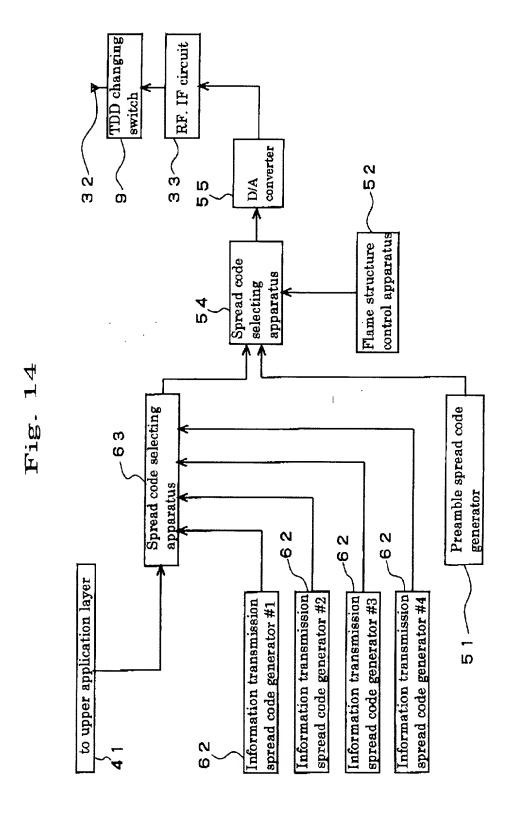


Fig.

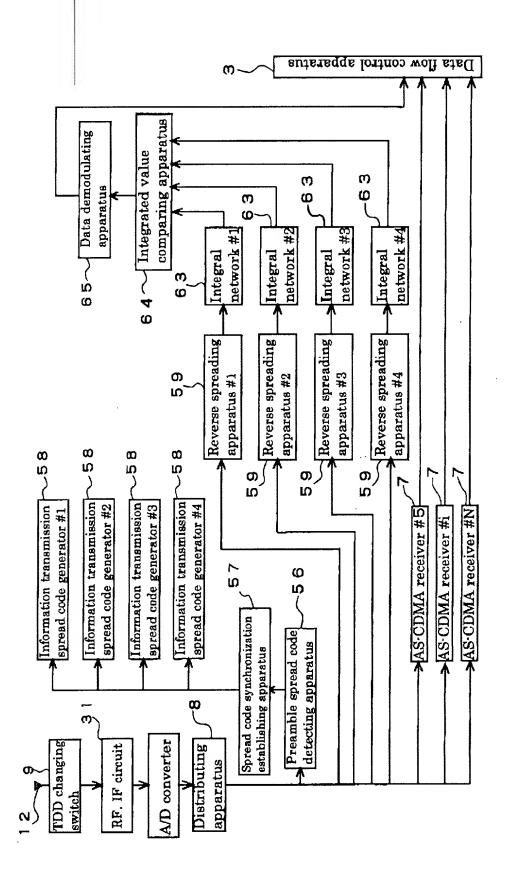
Data flow control apparatus synthesizing apparatus Data Ω, 7 AS-CDMA receiver Data demodulating Data demodulating **0** apparatus #1 apparatus #2 ιύ Ω rύ α Information transmission spread code generator #2 Information transmission spread code generator #1 S O Reverse spreading Reverse spreading apparatus #2 apparatus #1 9 57 D D O establishing apparatus Preamble spread code detecting apparatus synchronization → AS-CDMA receiver #N ► AS-CDMA receiver #3 ➤ AS-CDMA receiver #i Spread code TDD changing A/D converter RF. IF circuit Distributing apparatus switch (n)

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15 Fig.



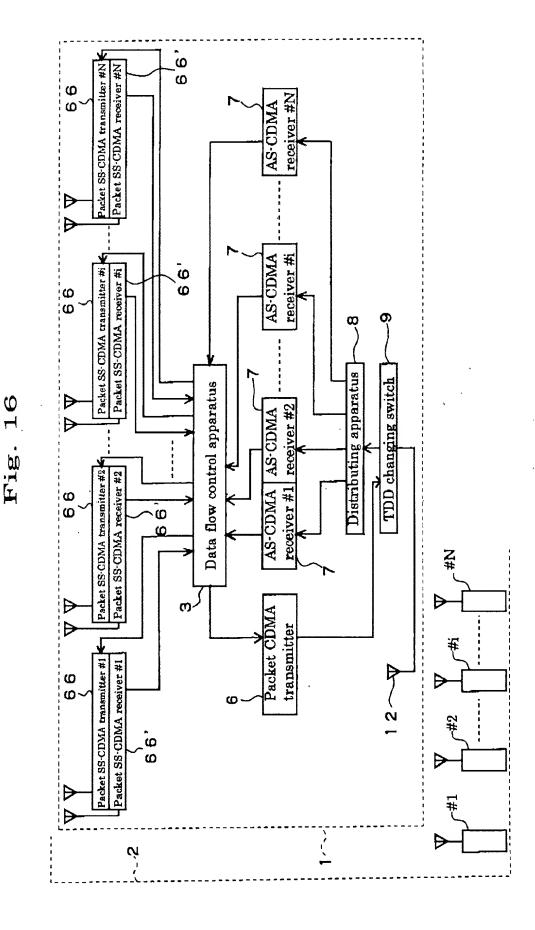


Fig. 17(a)

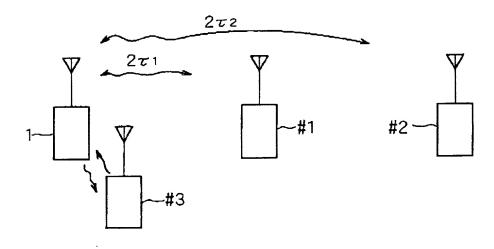


Fig. 17(b)

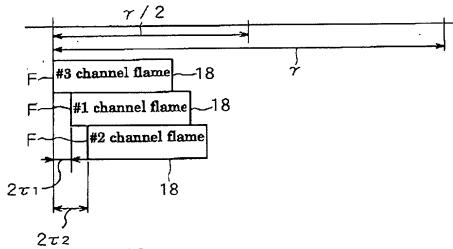


Fig. 17(c)

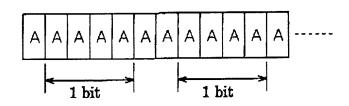
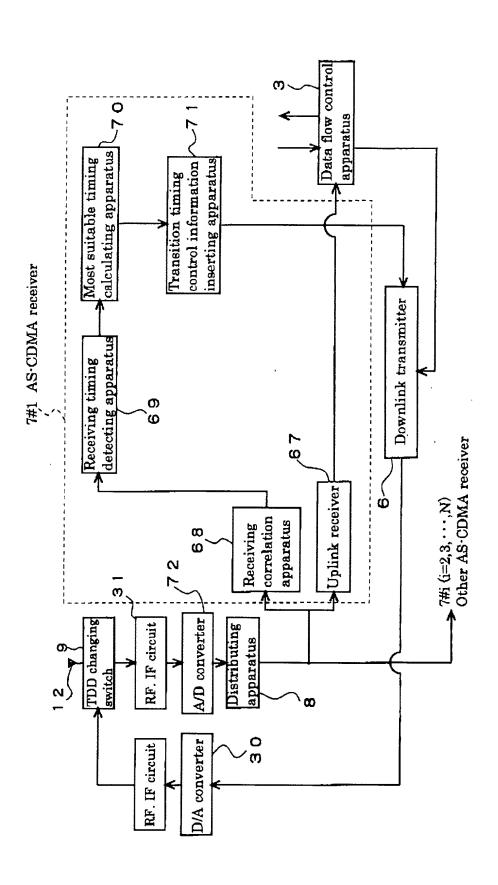


Fig. 18



extracting apparatus 4 4 Data demodulating control information application layer network #1 Integral apparatus Transition to upper timing 42 73 Reverse spreading apparatus #1 Information transmission spread code generator #1 9 8 ထ က Uplink transmitter Fig. establishing apparatus Preamble spread code 3 detecting apparatus 7.4 synchronization വ Spread code ώ ω A/D converter TDD changing RF. IF circuit **switch** 4 ന D/A converter 7 RF. IF circuit ന

Fig. 20(a)

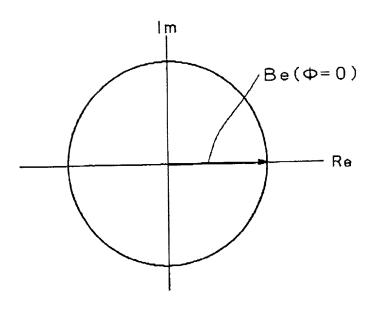


Fig. 20(b)

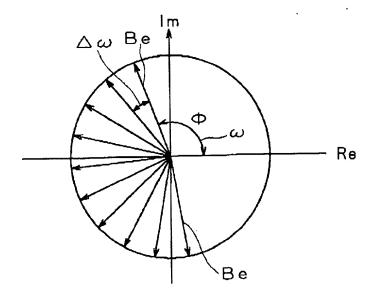


Fig. 21

